I B.Tech - II Semester – Supplementary Examinations DECEMBER 2024

ELECTRICAL CIRCUIT ANALYSIS-I (ELECTRICAL & ELECTRONICS ENGINEERING)

Duration: 3 hours

Max. Marks: 70

Note: 1. This question paper contains two Parts A and B.

2. Part-A contains 10 short answer questions. Each Question carries 2 Marks.

PART - A

- 3. Part-B contains 5 essay questions with an internal choice from each unit. Each Question carries 10 marks.
- 4. All parts of Question paper must be answered in one place.

1.a)	Define junction.		
1.b)	Differentiate between mesh and loop.		
1.c)	What role does the coefficient of coupling play in mutual		
	inductance?		
1.d)	Define co-efficient of Coupling.		
1.e)	Define form factor.		
1.f)	Define R.M.S value.		
1.g)	Define Q-Factor of a coil.		
1.h)	Draw the locus diagram for series RL circuit.		
1.i)	Draw the Norton's equivalent circuit.		
1.j)	Write the application of Thevenin's theorem.		

PART – B

			Max. Marks	
UNIT-I				
2	a)	State Kirchhoff's Laws.	3 M	
	b)	Using Mesh analysis, find the mesh currents in the	7 M	
		circuit.		





